

A					B					C					D					E					F				
5	4	3	2	1	5	4	3	2	1	5	4	3	2	1	5	4	3	2	1	5	4	3	2	1	5	4	3	2	1
LS 08 Full	LS 74 1,2	✓ 1Kx8 220/330 (Full)	LS 279 ✓	LS 244 ✓		✓ 1Kx8 (4B)		✓ 1Kx8 (2B)			✓ 1Kx8 (4C)		LS 273 ✓			(4D)		(2D)		✓ 416-15	✓ 416-15	✓ 416-15	✓ 416-15	✓ 416-15				✓ 416-15	416-15 + 8 G-16
S 240 ✓ 1,2,3,4	LS 240 Full ✓	7416 1	7416 Full	7416 Full		✓ 1Kx8 (9B)		✓ 1Kx8 (7B)		LS 32 1,2,1	20 MHz OSC	S 113	S 139 ✓			40 PIN		40 PIN		✓ 416-15	✓ 416-15	✓ 416-15	✓ 416-15	✓ 416-15					
LS 32 4,3 1,2	S86 4,3 1,2	✓ Header 16 pins V-16 G-8	LS 08 4,3 1,2	LS00 3 1,2						LS 174 ✓	LS 273 ✓	LS 174 ✓	LS 273 ✓	11		(14D)		(12D)		✓ 1-8		✓ 204	✓ 416-15	✓ 416-15					
LS 240 ✓ 1,2,3,4	LS240 Full ✓	✓ 220/330 Full	SW 8 pins 1,2	SC8 1,2		✓ 2911	2911	2911	LS 244 ✓	LS373 ✓	LS373 ✓	LS374 ✓	LS374 ✓			40 PIN		40 PIN											
LS 373 ✓	LS 374 ✓	LS 374 ✓	S 280 6 20 30	LS 74	S 251 ✓	S 251 ✓	S 86 1	S 174 ✓	LS 244 ✓	LS174 ✓	7416	LS174 ✓	7416		LS174 ✓	S280	S280	LS374 ✓	LS374 ✓							SW1 104 1	SW2 103 3 pos	SW3 104 2 pos	101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120

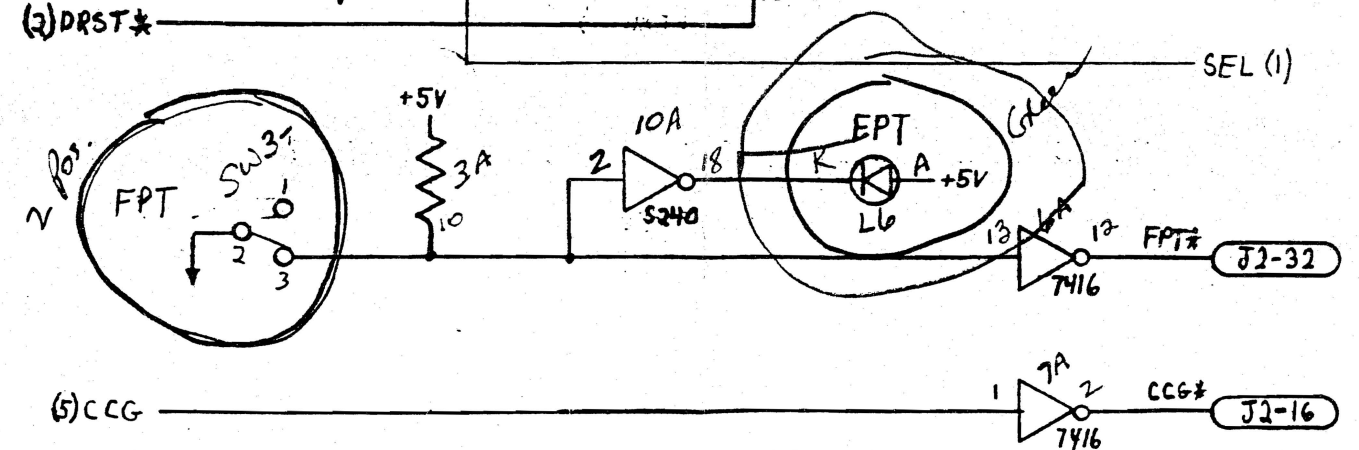
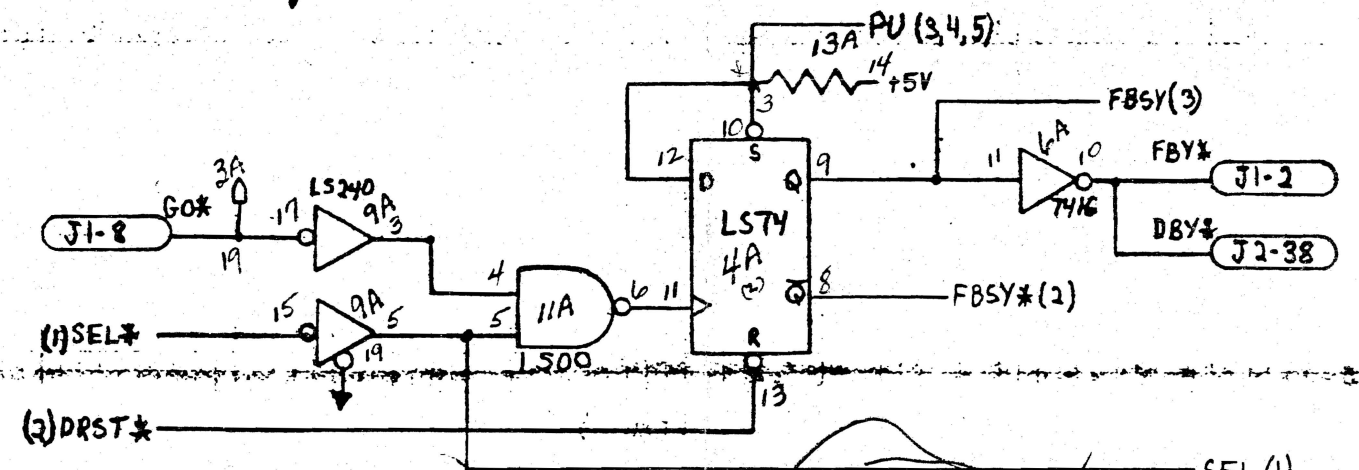
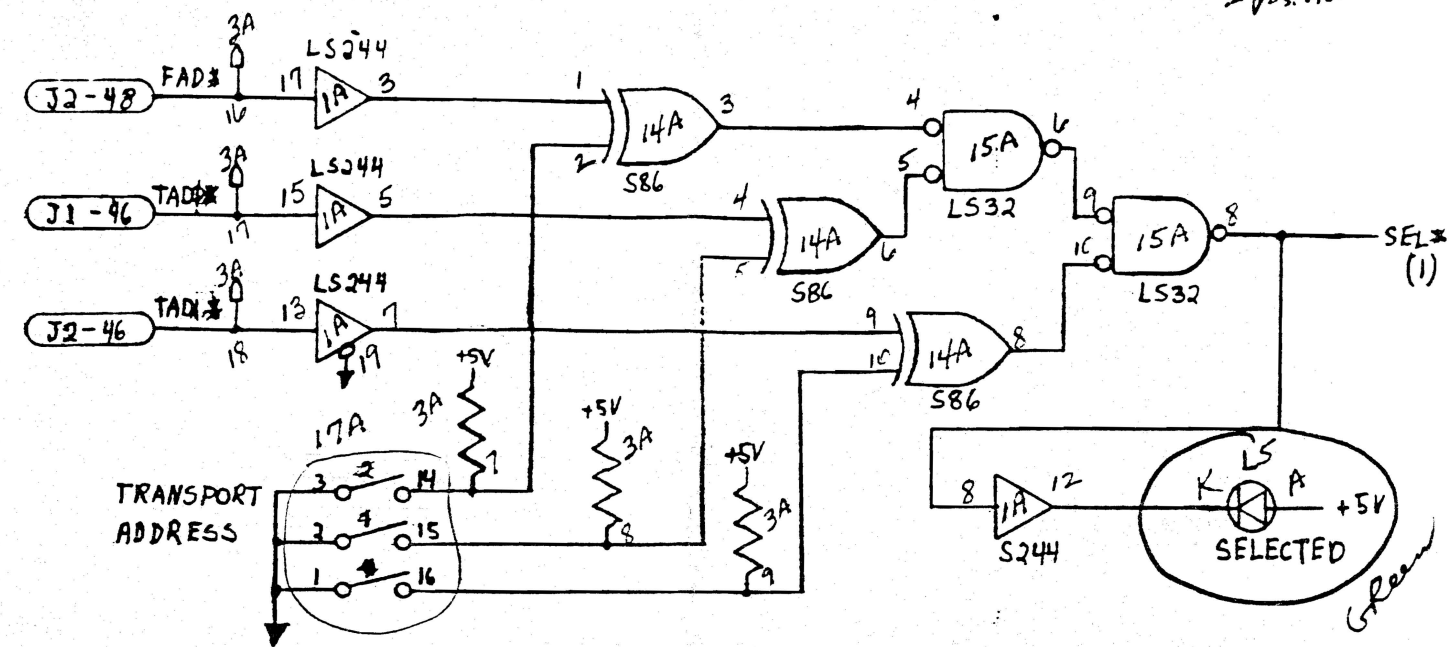
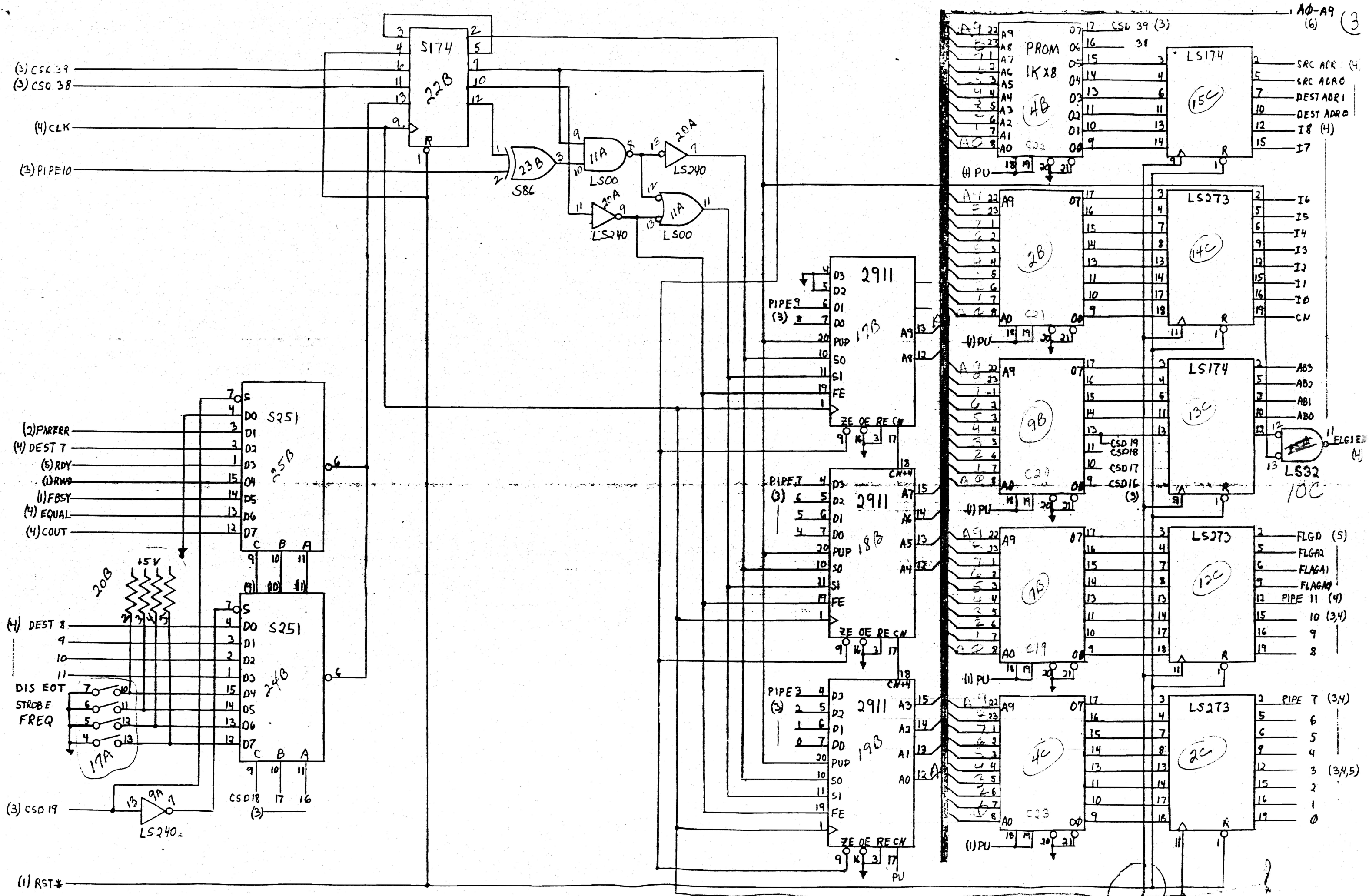
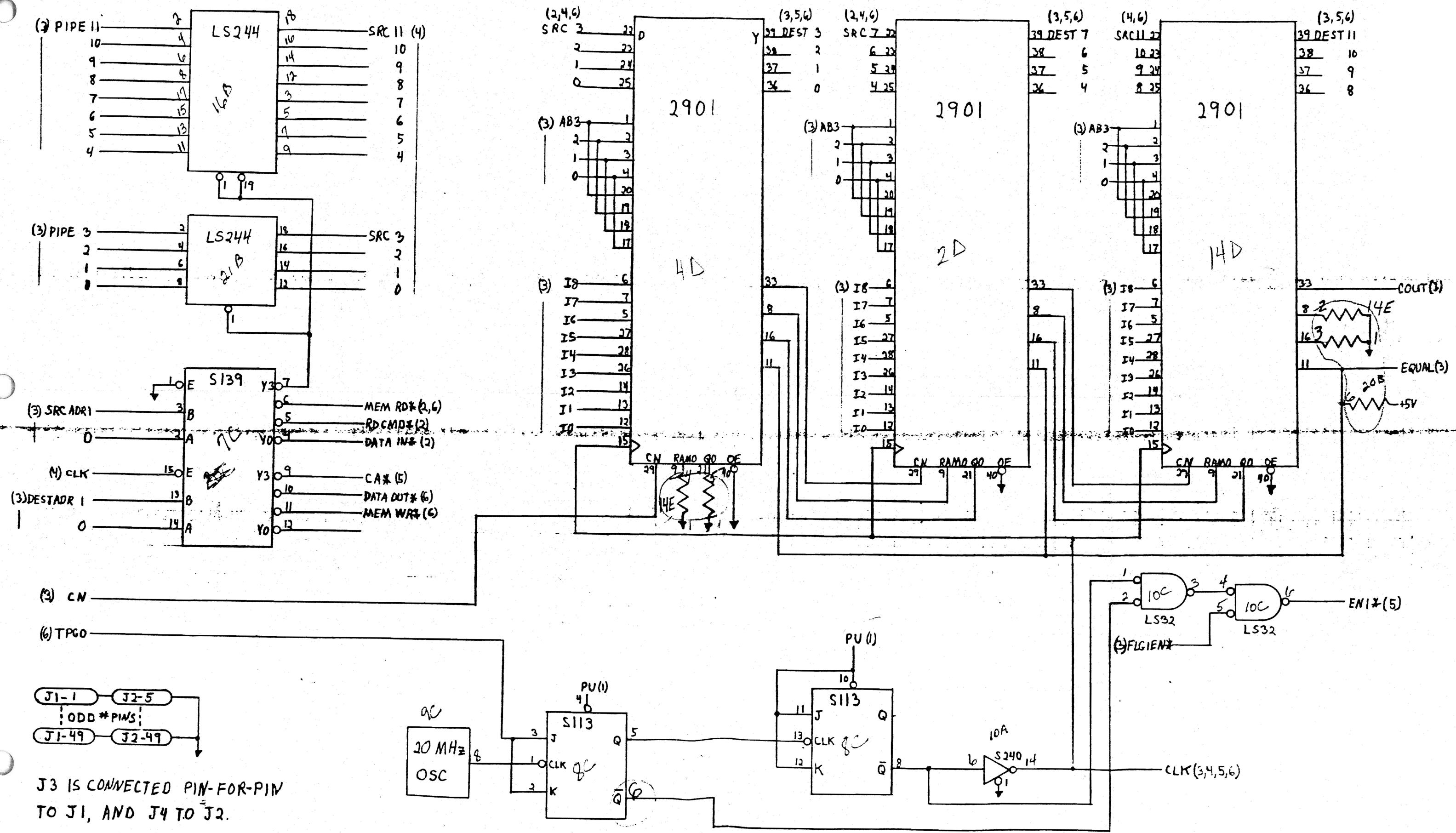


Diagram for problem 1: A 10V source is connected in series with a 120 ohm resistor and a 330 ohm resistor. The output voltage is measured across the 330 ohm resistor.

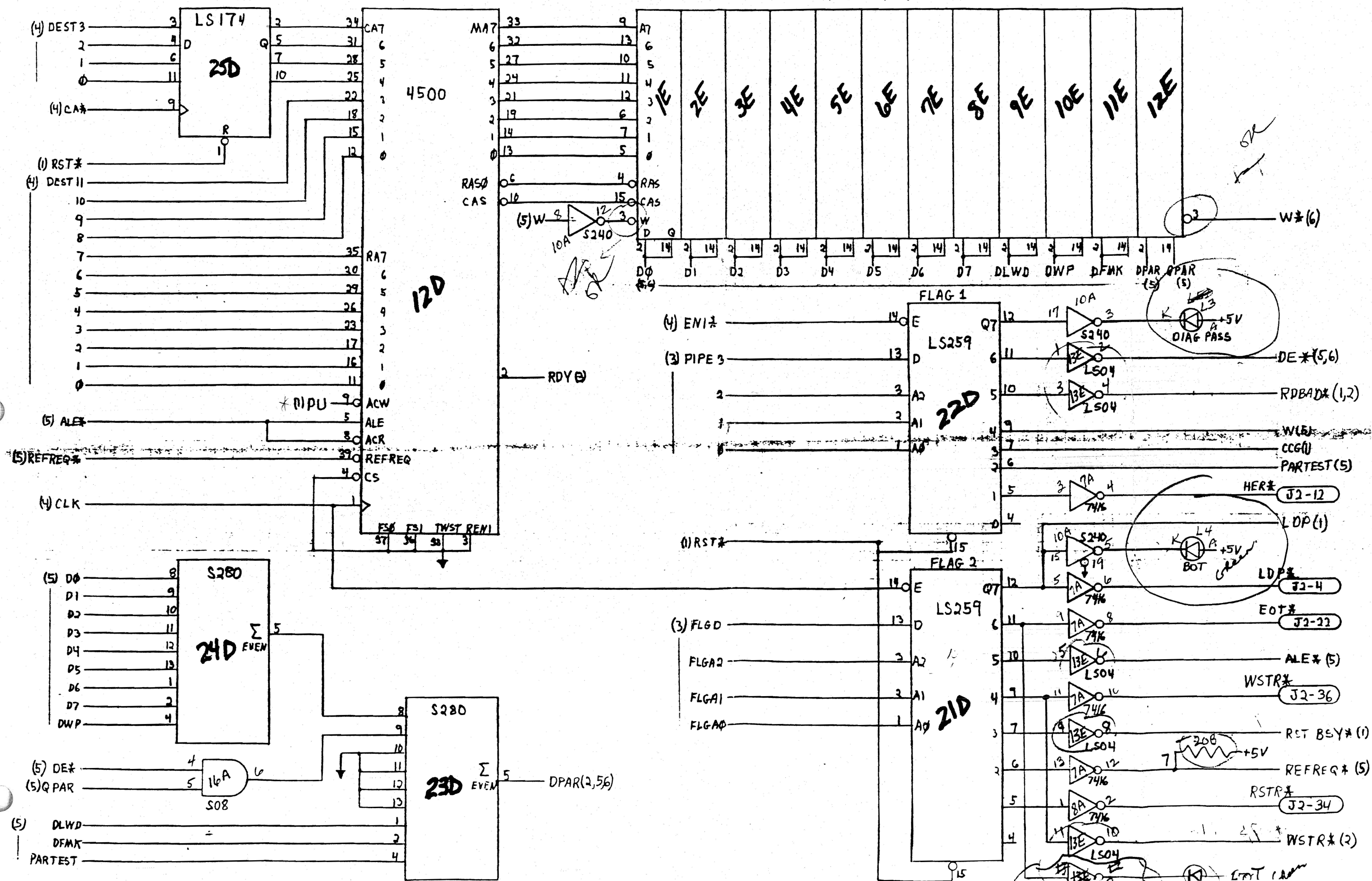
DAVID
526 HURON
415 964-

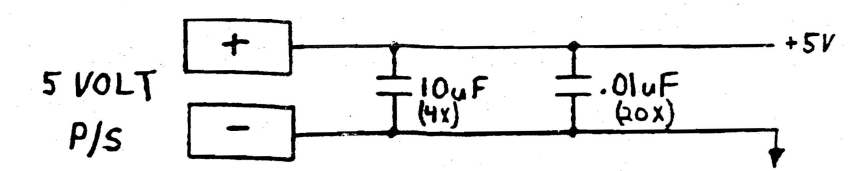
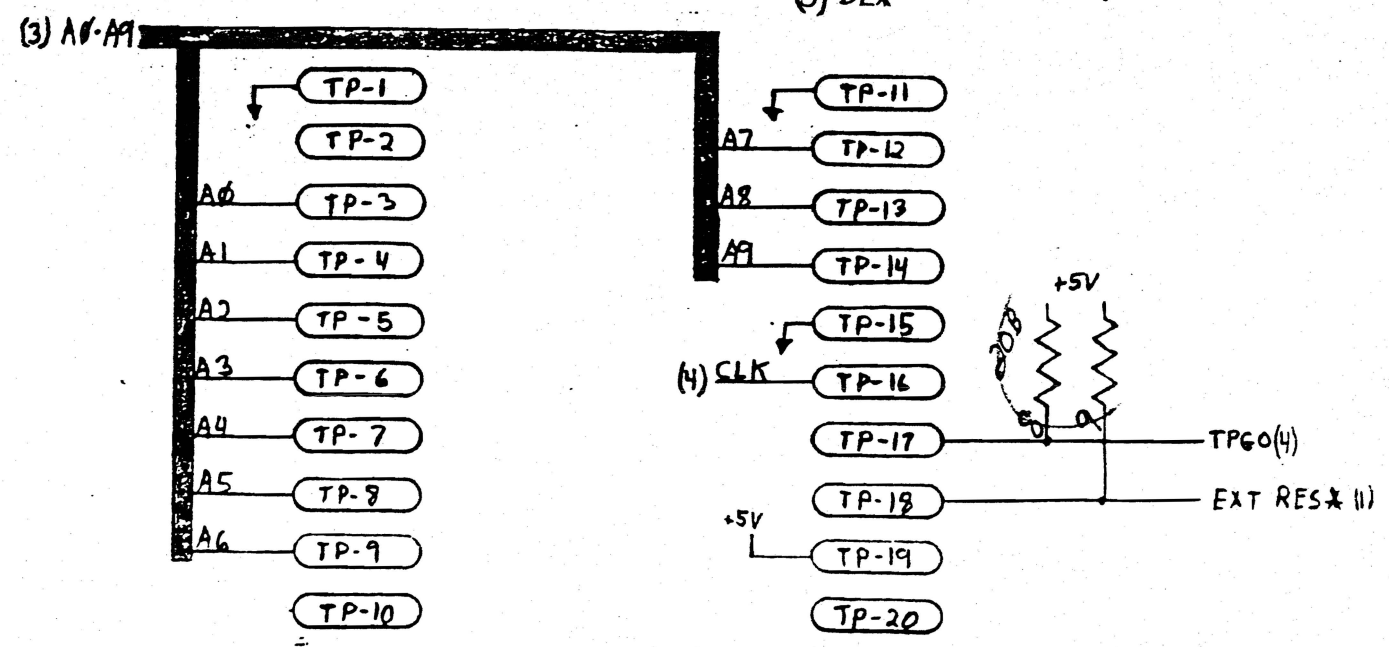
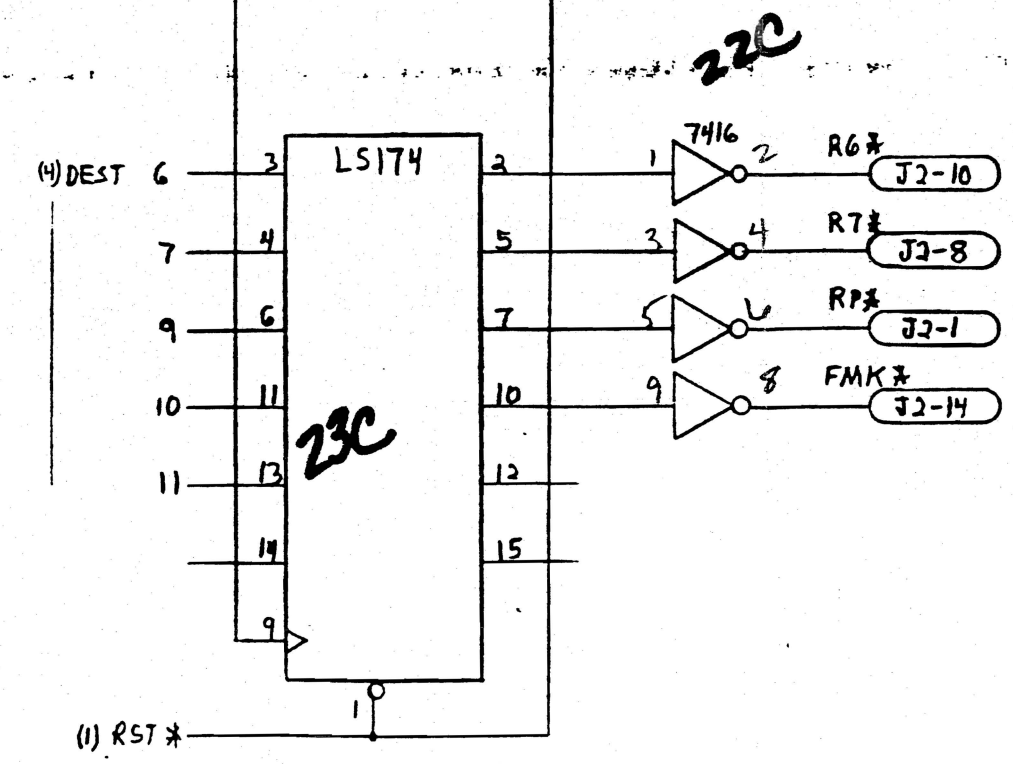
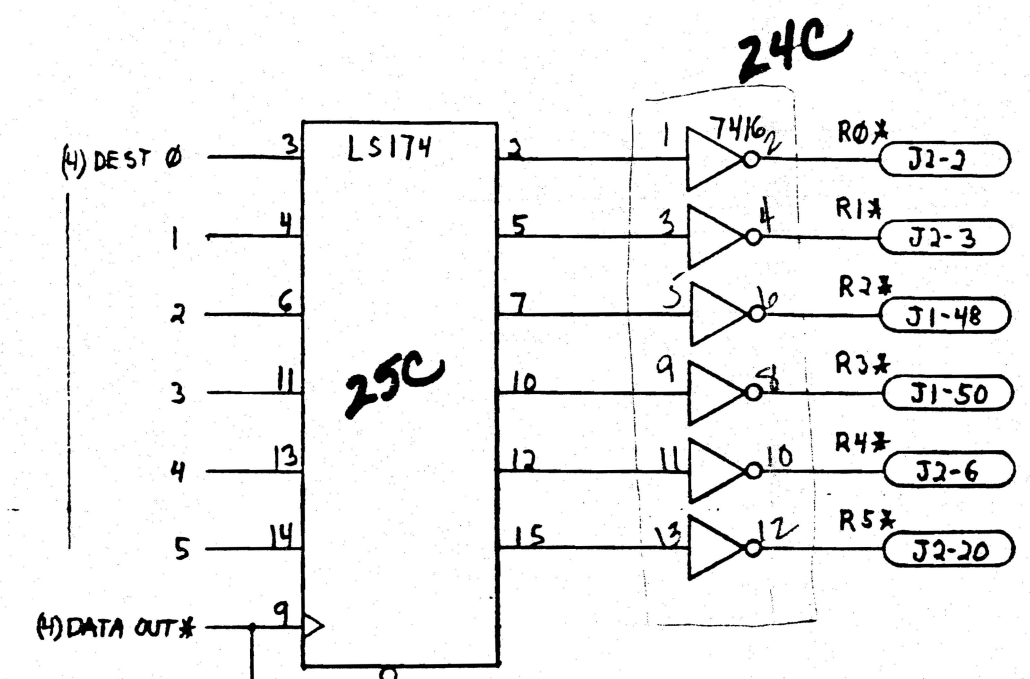
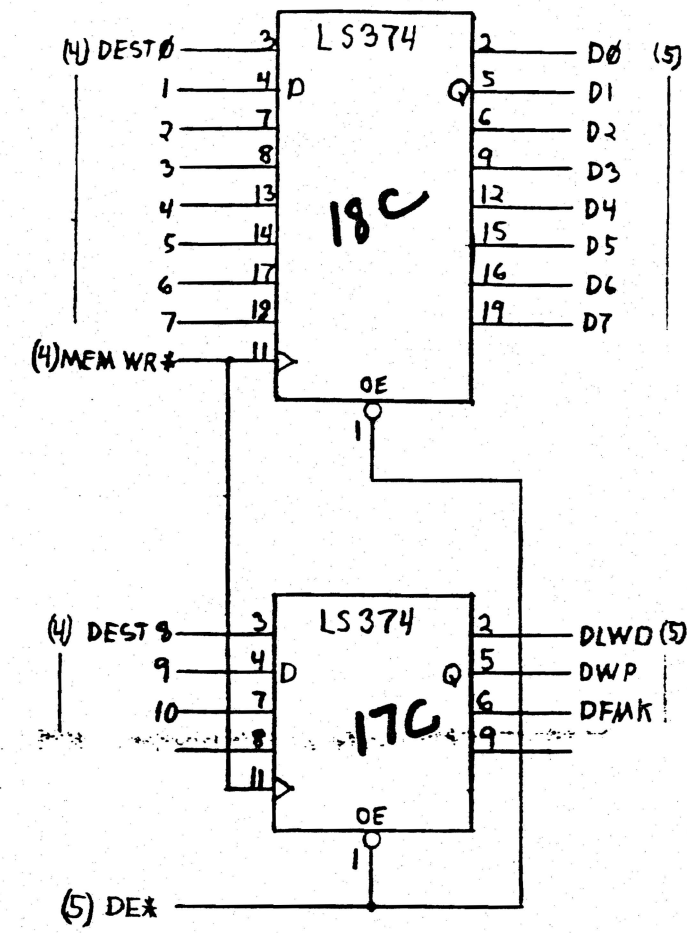
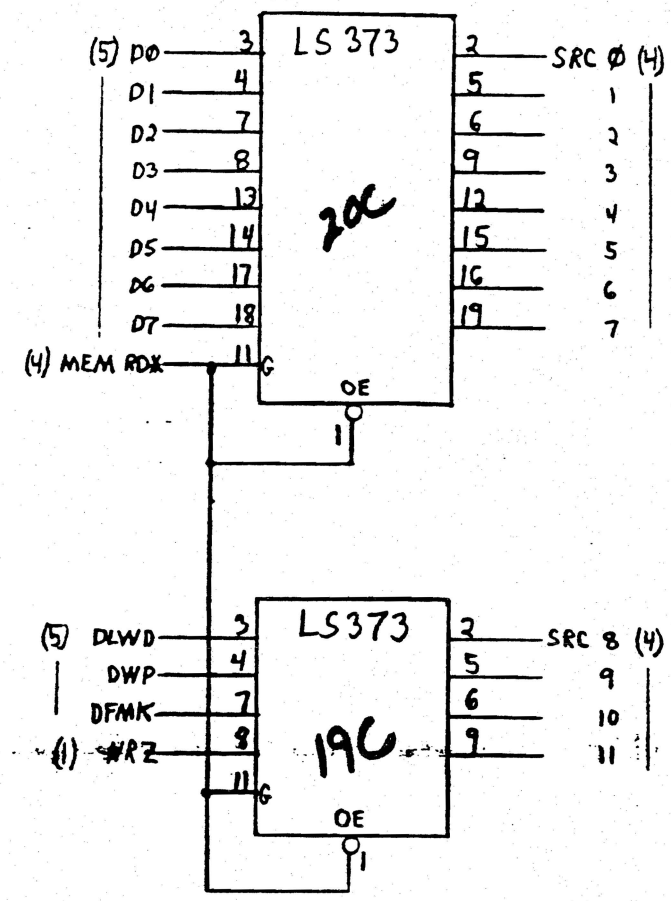
ALL RESISTORS ARE
1K Ω , 1/4W, 5%, UNLESS
OTHERWISE SPECIFIED.
PU (PG1) IS DISCRETE,
OTHERS ARE SIPS.

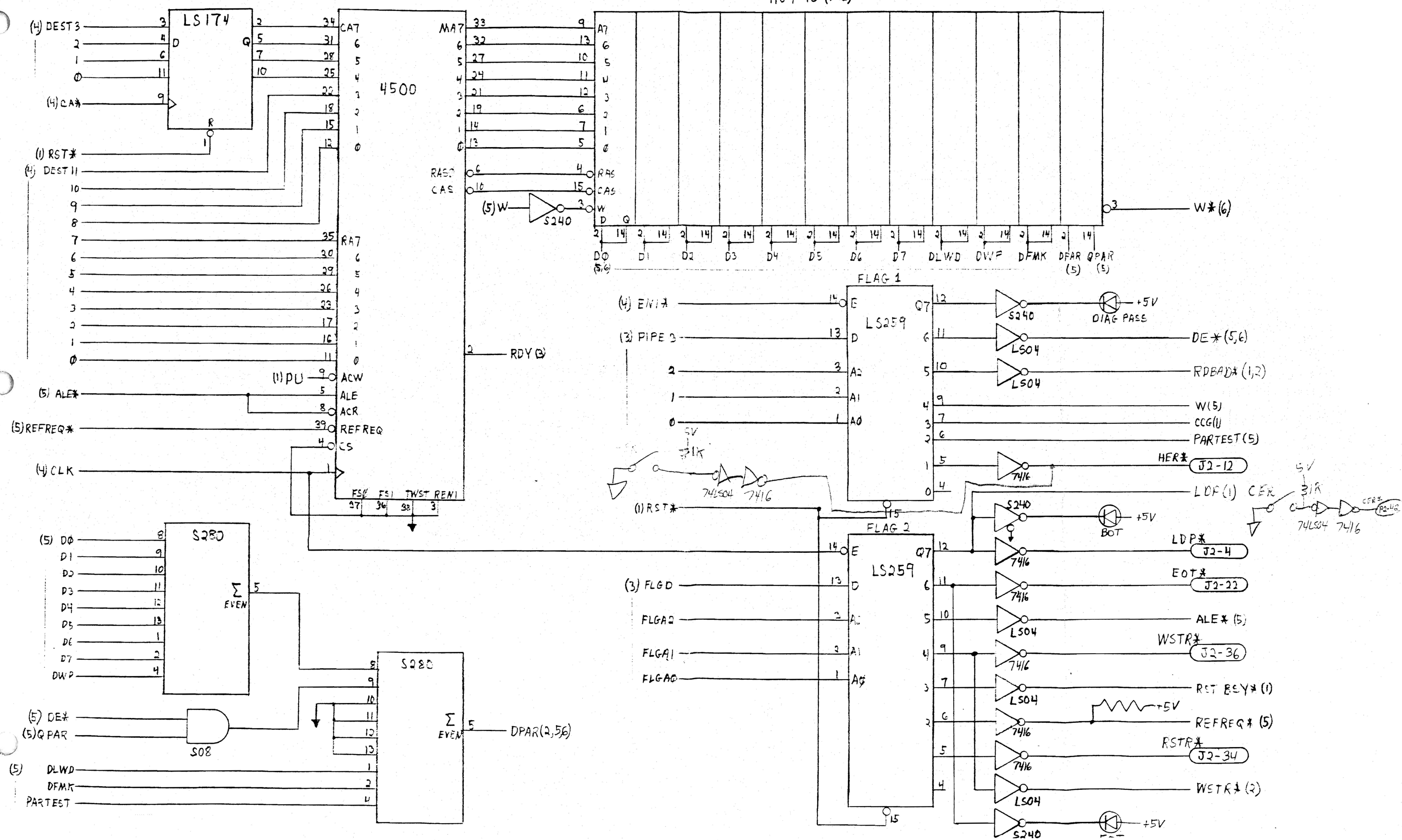


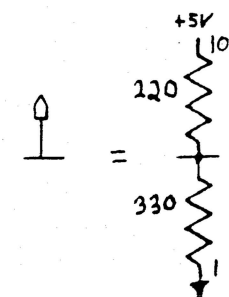
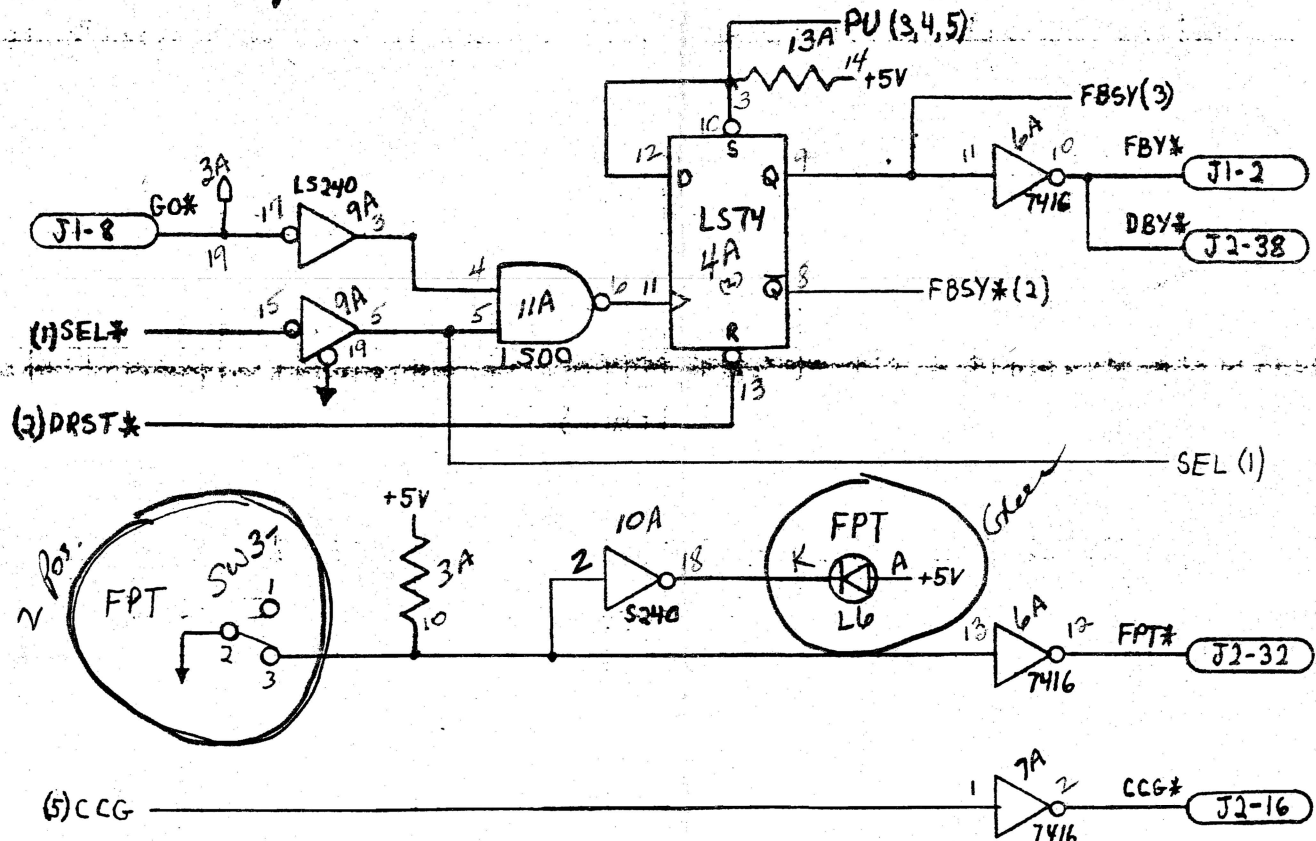
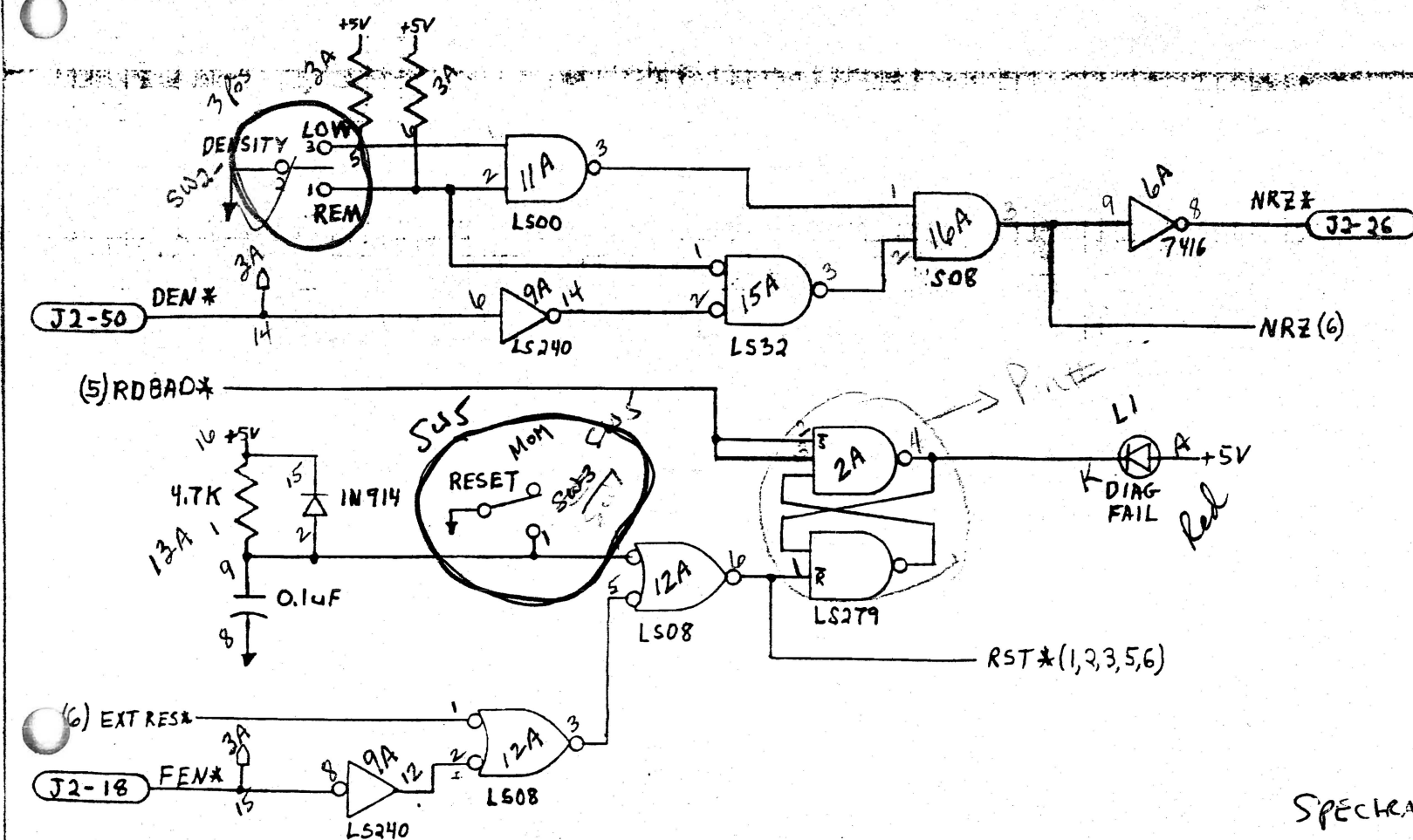


J3 IS CONNECTED PIN-FOR-PIN TO J1, AND J4 TO J2.



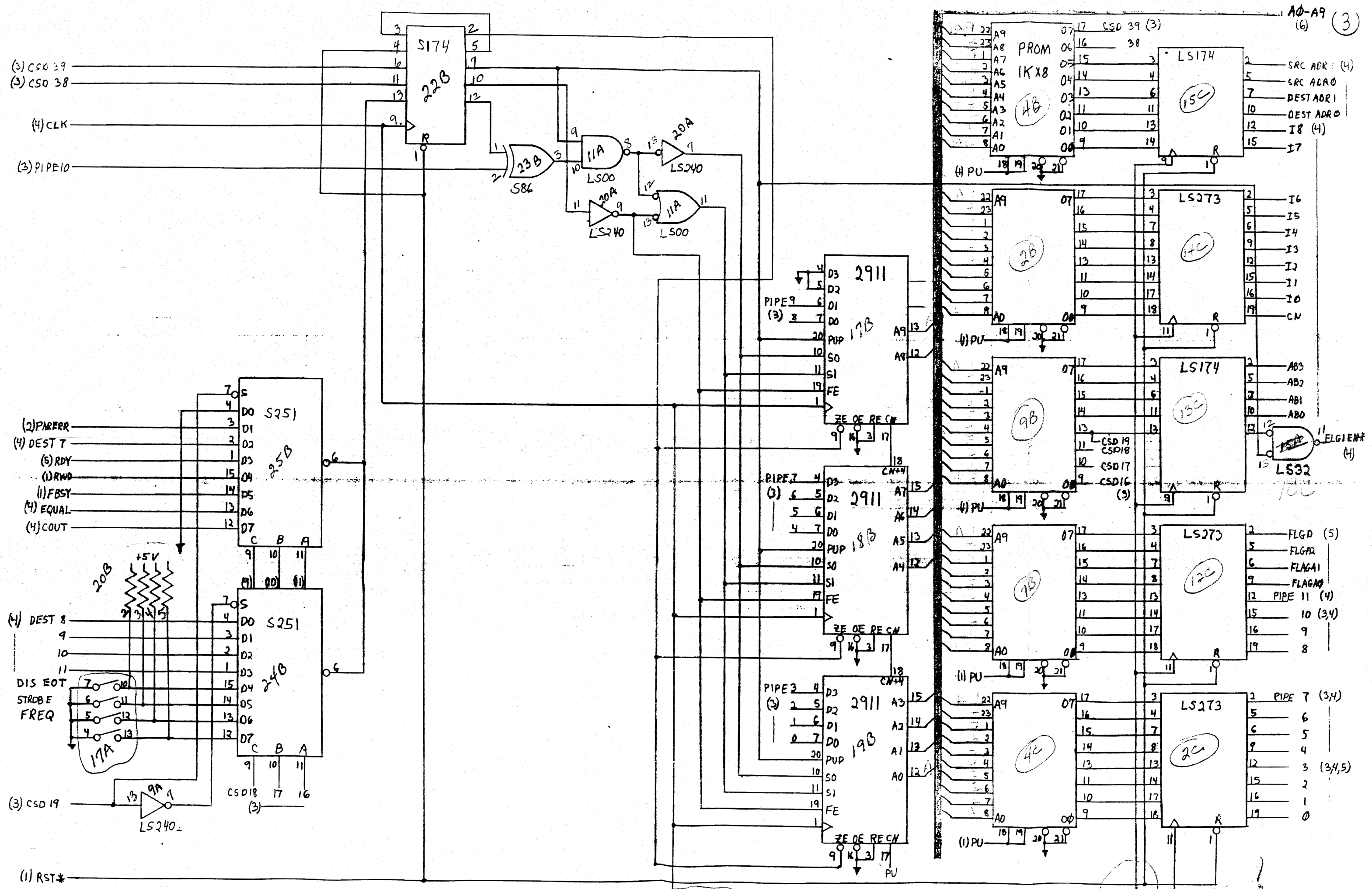


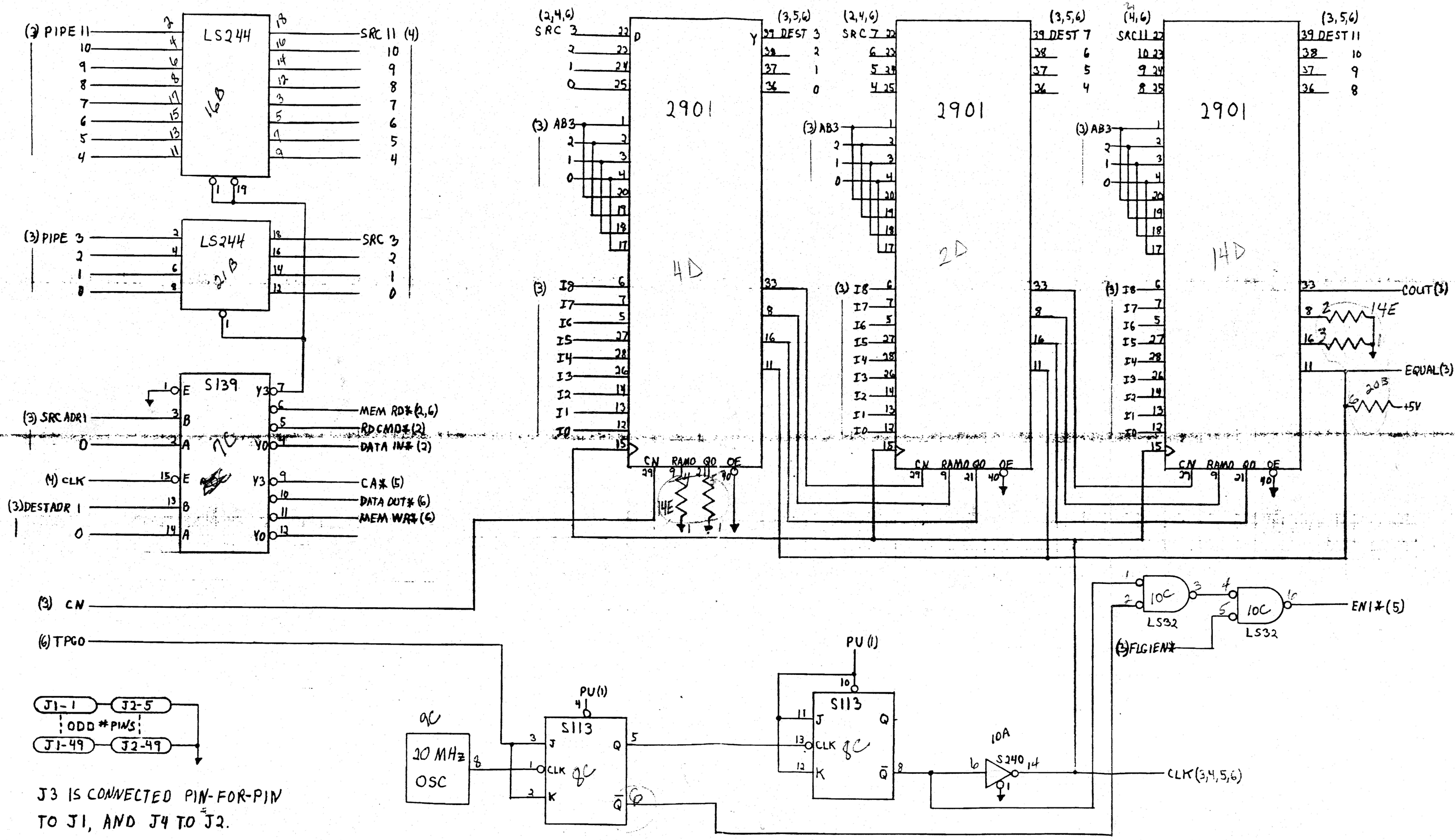




ALL RESISTORS ARE
1K Ω , 1/4W, 5%, UNLESS
OTHERWISE SPECIFIED.
PU (PG1) IS DISCRETE,
OTHERS ARE SIPS.

SPECTRA LOGIC
TAPE WORK





J3 IS CONNECTED PIN-FOR-PIN TO J1, AND J4 TO J2.

